

## REMARKS

With respect to the objection under Section 112, it is noted that the flash array 22 “is not directly coupled to the PCI bus 54.” See office action, paragraph 3. However, the claim simply requires coupling, not direct coupling. Clearly, in the illustrated embodiment, the flash memory is coupled to the bus through the FPGA 20a.

Therefore, reconsideration is requested.

With respect to the rejection under Section 102 of claims 21-25 as being anticipated by Klein, it is noted that the claim requires that the controller modify the address of the boot device. This never happens in Klein. In Klein, the address of the boot device is always the ROM BIOS which is on the PCI bus. It is noted in the office action that the chipset 114 normally generates an ISA address for the boot device. But there is no support for this. In fact, in the case of Klein, there is no modification of the boot address. The boot address is always the ROM BIOS on the PCI bus. While traditionally, the ROM BIOS may have been on the ISA bus, there is nothing in Klein which teaches modifying the address of the boot device from the ISA bus to the PCI bus.

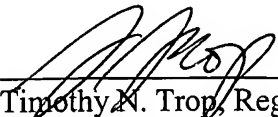
In the case of Klein, there is no reason to believe anything other than that Klein was designed, from the beginning, to supply the address of the boot device as being on the PCI bus. Therefore, there is no controller that can modify the address of the boot device and every reason to understand that the address of the boot device in Klein is fixed. Certainly, nothing in any of the cited material suggests anything to the contrary. In fact, the material at column 4, lines 32-59 does not even mention an ISA bus. Therefore, the material cited in support of this contention fails to support it. Likewise, the material in column 5, lines 4-23 also never mentions any modification of any address or any changing of the address of the boot device from the ISA to the PCI bus. There is every reason to believe that because it was known in Klein that the device was going to be on the PCI bus, the address of the boot device was always set to the PCI bus.

Further, it is noted that by configuring the system, as shown in Figure 3, the need for an ISA bus may be totally eliminated in Klein. See column 5, lines 24-27. This further confirms that there would be no basis for modifying the address of the boot device using a controller since there is no way that in such a system the boot device could have ever been on the ISA bus (which has been eliminated).

Reconsideration is requested.

Respectfully submitted,

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